

REMARKS

Applicant has thoroughly reviewed the outstanding Office Action including the Examiner's remarks and the references cited therein. The following remarks are believed to be fully responsive to the Office Action and render all claims at issue patentable and distinguishable over the cited references.

Reconsideration and withdrawal of the rejections set forth in the Office Action dated Dec. 01, 2006 are respectfully requested.

I. Claim Rejections - provision of 35 U.S.C. 103 (a)

The Examiner rejected Claims 1-6, 8, 10-15, 17-19, 21-26 and 30-34 of the present application under 35 U.S.C 103 (a) as being unpatentable over U.S. Patent No. 6549257 (hereinafter referred to as Liu) and U.S. Patent No. 6724452 (hereinafter referred to as Takeda *et al.*).

Referring to the references cited by the examiner, Liu discloses a multi-domain wide viewing angle liquid crystal display (LCD) having a plurality of superimposed spacing wall-bump structures formed on a pair of parallel substrates. Takeda *et al.* discloses a strip patterns can be formed as a bump and/or slit structure. Takeda *et al.* discloses that bumps and slits can be interchangeably formed over the substrate since both bumps and slits have the same function of aligning liquid crystal molecules in each domain. The examiner believes that combination the cited references render the applicant's invention obviousness.

To sum up, the examiner's rejection is based on that bumps and slits discloses by the references are interchangeable in achieving the function of aligning liquid crystal molecules in each domain.

Referring to the page 10 line 36 and the descriptions below of present application, if the bumps are only formed on the substrate having the common electrode layer, the problem that bumps are hard to be formed on a substrate having a plurality of transistors is solved; therefore, the yield can be raised and the cost can be reduced. This technical feature shows that a bump and a slit are not interchangeable as set in the amended claims 1 and

25. Both claimed inventions indicate that the bumps are formed on the first substrate having a common electrode layer and said multi domain pattern, and transistors are formed on a second substrate with a plurality of strip patterns having slit structures. Hence the prior art, that is, bumps and slits are interchangeable discloses by the references dissuade person having ordinary skill in the art (PHOSITA) from thinking of the technical feature, that is, bumps are only formed on the substrate having the common electrode layer, disclosed in the present application; i.e., the references cited by the examiner teach away the PHOSITA from combining the references to render this technical feature of the present application obvious.

Hence, referring to the amendments to the claims of the present communication, the Claim 1 has been appropriately amended to render the present application unobviousness over the references cited by the examiner. Thus, the rejection of Claim 1 under U.S.C. 103(a) is overcome, for the same reason, so are the claims 2-6, 8 10-15 17-19 and 21-26, 28 and 30-34.

II. Conclusion

In view of the foregoing, Claims pending in the application comply with the requirements of patentability define over the applied art. A Notice of Allowance is, therefore, respectfully requested.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 50-0665, under Order No. 386998051US from which the undersigned is authorized to draw.

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Respectfully submitted,

By 

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